	 	 		_
THE RESERVE TO SERVE THE RESERVE				
Reg. No. :				
neg. No. :				
				-

## Question Paper Code: 21455

## B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2015.

Fifth Semester

Electronics and Communication Engineering

## EC 2304/EC 54 - MICROPROCESSORS AND MICROCONTROLLERS

(Regulations 2008)

(Common to PTEC 2304 – Microprocessors and Micro Controllers for B.E. (Part-Time) Fifth Semester Electronics and Communication Engineering Regulations 2009)

Time: Three hours Maximum: 100 marks

Answer ALL questions:

PART A  $-(10 \times 2 = 20 \text{ marks})$ 

- Why is the 8086 memory divided into odd and even banks?
- 2. What do you mean by segment override prefix?
- 3. What are the 8086 instructions used for BCD arithmetic?
- 4. What are the contents of AL and CY after the execution of the following segment?

MOV BL, D5H

RCL BL, 3

MOV AL. BL.

- 5. What is a sample and hold circuit?
- 6. What is key-debouncing?
- 7. How does the processor 8051 knows whether on chip RCM or external program memory is used?
- 8. What is the difference between AJMP and LJMP instruction?
- 9. Why are relays that use coils called electromagnetic relays?
- 10. What is PWM?

## PART B — (5 × 16 = 80 marks)

			(0 × 10 = 80 marks)	
	11.	(a)	(i) The data transfer rate of I/O device 'A' is contained of the microprocessor. Draw a flowchar operation to be used.	siderably less than t of data transfer
			(ii) Describe the functions of execution unit and bus	interface umt. (8)
		(b)	Explain the following:	1,210
		1000111		
			mode tu oogo	(8)
-3	2		ap processing	
14	41	(a)	What do you mean by assembler directives? Ex TYPE, OFFSET with suitable examples.	plain SEGMENT,
		13	<li>Write an 8086 ALP to check whether the given st or not.</li>	(8)
			or not.	
			Or	(8)
	(1	5) (	<ul> <li>Write an 8086 ALP to separate odd and even nu array.</li> </ul>	mbers in a given
		(6		
			ii) Explain the data transfer group and logical instructions with necessary examples.	
-13	(a	9 (/	With neat block diagram, explain the 8255 programs sterface (PPI) and its operating modes.	(10) nable periphera
			Or	(16)
	(b)	) Di	raw and explain the block discuss a sec-	nmable Interval
14.	(a)		Draw the pin diagram of 8051 missions it	The state of the s
		(ii)		
		1000	Discuss in brief the various registers in 8051 microc	ontroller (6)
	(b)	(i)	Explain the interfacing of 4 × 4 matrix keybosi microcontroller with a neat diagram.	rd to the 8051
		(ii)	The state of the s	
5.	(a)	(i)	With a nest diagram, explain washing and	(6)
		760	A STATE OF THE PARTY OF THE PAR	The second secon
		(ii)	With a diagram, explain the DC motor contro	l using 8051
			Or	(8)
	(b)	(i)	Explain stepper riotor control using 8051 microcontrol	W 50
		(ii)	With a neat diagram evalua at pro-	ller. (8)
	16		With a neat diagram, explain the RTC interfacus	ng using 12C