



Department of Electrical and Electronics Engineering

EE8691- Embedded Systems

Unit II - MCQ Bank

1. The input and output operations are respectively similar to the operations,
A) Read, Read
B) Write, Write
C) **Read, Write**
D) Write, Read

Answer: (C)

2. While performing read operation, one must take care that much current should not be
A) Sourced From Data Lines
B) Sunked From Data Lines
C) **Sourced Or Sunked From Data Lines**
D) Sunked From Address Lines

Answer: (C)

3. In memory-mapped scheme, the devices are viewed as
A) Distinct I/O Devices
B) **Memory Locations**
C) Only Input Devices
D) Only Output Devices

Answer: (B)

4. _____ are used to overcome the difference in data transfer speeds of various devices.
A) Speed Enhancing Circuitry
B) Bridge Circuits

C) Multiple Buses

D) Buffer Registers

Answer: (D)

5. The bus used to connect the monitor to the CPU is _____

A) Pci Bus

B) Scsi Bus

C) Memory Bus

D) Rambus

Answer: (B)

6. The main advantage of multiple bus organization over a single bus is _____

A) Reduction In The Number Of Cycles For Execution

B) Increase In Size Of The Registers

C) Better Connectivity

D) None Of The Mentioned

Answer: (A)

7. Which of the following can affect the long distance communication?

A) Clock

B) Resistor

C) Inductor

D) Capacitor

Answer: (A)

8. Which of the following can provide hardware handshaking?

A) RS232

B) Parallel Port

C) Counter

D) Timer

Answer: (A)

9. Which of the following have an asynchronous data transmission?

A) Spi

B) RS232

C) Parallel Port

D) I2c

Answer: (B)

10. The RS232 is also known as

A) Uart

B) Spi

C) Physical Interface

D) Electrical Interface

Answer: (D)

11. Which protocol standard of serial communication specify the bi-directional and half-duplex form of data transmission by allowing various numbers of drivers and receivers in bus configuration?

A) RS232

B) RS2485

C) RS422

D) RS423

Answer: (B)

12. What is the maximum device handling capacity of serial standard protocol RS485 in terms of drivers and receivers on a single line?

A) 8

B) 10

C) 16

D) 32

Answer: (D)

13. What does an IC that initiate or enable the data transfer on bus can be regarded as, in accordance to the I2c protocol specifications?

A) Bus Master

B) Bus Slaves

C) Bus Drivers

D) Bus Data Carriers

Answer: (A)

14. What is the directional nature of two active wires SDA & SCL usually adopted in I2C Bus for carrying the information between the devices?

A) Uni-directional

B) Bi-directional

C) Multi-directional

D) None of the above

Answer: (B)

15. Which potential mode of operation indicate the frequent sending of byte to the slave corresponding to the reception of an acknowledge signal when it becomes desirable for the master to write to the slave during data transmission in I2C bus?

A) Master in master-transmit mode & Slave in slave-receive mode

B) Slave in slave-transmit mode & Master in master-receive mode

C) Master in master-transmit mode as well as master-receive mode

D) Slave in slave-transmit mode as well as slave-receive mode

Answer: (A)

16. CAN means

- A) Center Area Network
- B) Conversion Analog Network
- C) Control Area Network**
- D) Communication Area Network

Answer: (C)

17. Which allows the full duplex synchronous communication between the master and the slave?

- A) SPI**
- B) Serial Port
- C) I2C
- D) Parallel Port

Answer: (A)

18. What happens when 8 bits are transferred in the SPI?

- A) Wait Statement
- B) Ready Statement
- C) Interrupt**
- D) Remains Unchanged

Answer: (C)

19. Which signal is used to select the slave in the serial peripheral interfacing?

- A) Slave Select**
- B) Master Select
- C) Interrupt
- D) Clock Signal

Answer: (A)

20. Which of the following is the most known simple interface?

- A) I2C**
- B) Serial Port

- C) Parallel Port
- D) SPI

Answer: (A)

21. Which are the two lines used in the I2C?

- A) SDA and SPDR
- B) SPDR and SCL
- C) SDA and SCL**
- D) SCL and status line

Answer: (C)

22. Which of the following are the three hardware signals?

- A) START, STOP, ACKNOWLEDGE**
- B) STOP, TERMINATE, END
- C) START, SCL, SDA
- D) STOP, SCL, SDA

Answer: (A)

23. Which of the following enables the user to utilise the system efficiently?

- A) Kernel
- B) Operating System**
- C) Software
- D) Hardware

Answer: (B)

24. Which of the following bus can easily upgrade the system hardware?

- A) Control Bus
- B) Data Bus
- C) Vmebus**
- D) Bus Interface Unit

Answer: (C)

25. Which of the following is an example of a single task operating system?

- A) Android
- B) Windows
- C) IOS
- D) CP/M**

Answer: (D)

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