



### INSIDE THE ISSUE

- ABOUT THE DEPARTMENT
- DEPARTMENT ACTIVITY
- FACULTY ACHIEVEMENT
- FACULTY CONTRIBUTION
- STUDENTS' ACHIEVEMENTS
- PLACEMENTS

### EDITORIAL BOARD

#### FACULTY MEMBERS

- Ms.D.RAGAVI, AP/ECE
- Ms.J.JENISHA, AP/ECE

#### STUDENTS

- A.SARAVANAKUMAR, IV ECE
- H.POOJA, IV ECE
- S.P.DEVASRI, IV ECE



The Department of Electronics and Communication Engineering was established in the year 2007. The department has an intake of 60 students in B.E. course. The department possesses the most advanced equipment in its laboratories. It also provides opportunities to grow and excel in the technical world by conducting regular workshops and programs in various fields. The department is highly active in research work in the fields of broadband communications, VLSI Design, image processing etc. The students are provided adequate training in the field of signal processing, image processing and digital communication. Students are highly encouraged to undergo in-plant training at some of the most prestigious organizations during their time at the institution.

### **Vision of the Department:**

1. To provide the quality education in the field of Electronics and Communication Engineering which caters the needs of the society in line with the technological revolution.

### **Mission of the Department:**

1. To upgrade the technical knowledge of the students continuously by providing industrial exposure and innovative projects.
2. To establish a creative learning environment for the students by active learning of the techniques in the electronics and communication engineering field.
3. To nurture career improvement by facilitating skill development and training in the recent technologies.

## PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

**PEO1:** To provide the students with a strong foundation in the required sciences in order to pursue studies in Electronics and Communication Engineering.

**PEO2:** To gain adequate knowledge to become good professional in electronic and communication engineering associated industries, higher education and research.

**PEO3:** To develop attitude in lifelong learning, applying and adapting new ideas and technologies as their field evolves.

**PEO4:** To prepare students to critically analyze existing literature in an area of specialization and ethically develop innovative and research oriented methodologies to solve the problems identified.

**PEO5:** To inculcate in the students a professional and ethical attitude and an ability to visualize the engineering issues in a broader social context.

## PROGRAM SPECIFIC OUTCOMES (PSOs)

**PSO1:** Design, develop and analyze electronic systems through application of relevant electronics, mathematics and engineering principles

**PSO2:** Design, develop and analyze communication systems through application of fundamentals from communication principles, signal processing, and RF System Design & Electromagnetism.

**PSO3:** Adapt to emerging electronics and communication technologies and develop innovative solutions for existing and newer problems.

## DEPARTMENT ACTIVITY

### FRESHER'S DAY

The Department of Electronics and Communication Engineering celebrated “**FRESHER’S DAY**” for Batch 18 on 3rd September 2025. To help the freshers familiarize with the college atmosphere and their seniors, several interactive and engaging activities like Message Relay, Connection and Flip the Bottle and, many fun-filled activities were conducted to encourage teamwork and camaraderie.



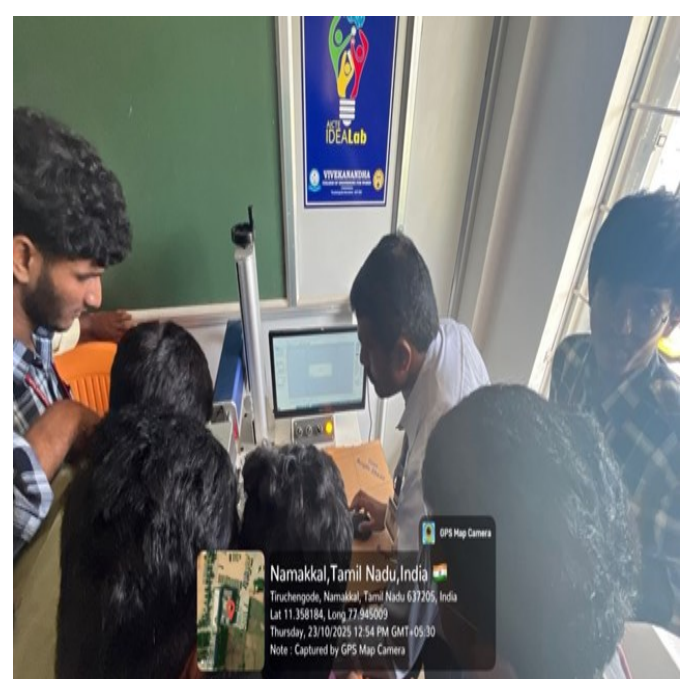
## BOOK FREE DAY

The Department of Electronics and Communication Engineering, Chettinad College of Engineering and Technology, Puliur, Karur, hosted a “**BOOK FREE DAY**” on October 17, 2025. This day was a grand amalgamation of technical and non-technical competitions aimed at providing a comprehensive platform for intellectual exploration and skill enhancement.



## AICTE IDEA LAB VISIT

On 23rd October 2025, 48 ECE II-year students and 2 faculty members visited the AICTE IDEA Lab at Vivekanandha College of Engineering for Women, Tiruchengode. The visit provided valuable exposure to cutting-edge technologies like AR, VR, and 3D printing. Students experienced immersive VR demonstrations and witnessed real-time 3D printing applications, including healthcare-related models. The visit enhanced their understanding of innovation and technology-driven learning. Overall, the experience inspired students to explore creative ideas and apply modern tools for solving real-world problems.





## FACULTY ACHIEVEMENT

Our **Head of the Department (HOD)** Dr. M. Kumar successfully completed the NPTEL Elite certification in **“Introduction to Programming in C”** offered by IIT Kanpur. He achieved an impressive **86%** consolidated score, with full marks in online assignments and strong performance in the proctored exam. This accomplishment reflects his dedication to continuous learning and academic excellence, inspiring both faculty and students.



Elite

### NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)

This certificate is awarded to

**M KUMAR**

for successfully completing the course

**Introduction to Programming in C**

with a consolidated score of **86** %

Online Assignments	25/25	Proctored Exam	60.64/75
--------------------	-------	----------------	----------

Total number of candidates certified in this course: 1771



**Skill India**  
कौशल भारत - कुशल भारत



*Patish*

**Prof. B. V. Ratish Kumar**  
Chairman, Centre for Continuing Education  
IIT Kanpur

Jul-Sep 2025

(8 week course)

*Satyaki*

**Prof. Satyaki Roy**  
NPTEL Coordinator  
IIT Kanpur



Indian Institute of Technology Kanpur



Roll No: NPTEL25CS119S539000393

To verify the certificate



No. of credits recommended: 2 or 3

## FACULTY CONTRIBUTION

Our department faculty Dr. S. Kokila delivered a **Guest Lecture** on the topic “**Electron Devices and Circuits**” to the students of **St. Mother Theresa Engineering College, Thoothukudi**, through **online mode** on **10.10.2025**. Students gained useful insights into key electronic components and circuit fundamentals.

**Department of Electronics & Communication Engineering**

Organizes

### Guest Lecture on Electronic Devices & Circuits

**SPEAKER**

**Dr. S. KOKILA**  
Associate Professor  
Chettinad College of Engineering and Technology



**St. MOTHER THERESA ENGINEERING COLLEGE**  
Approved by AICTE | Affiliated to Anna University  
NIRE INNOVATION RANKED INSTITUTION '23  
VAGAIKULAM - THOOTHUKUDI 628 102

★★★★★ MoE'S INNOVATION CELL (GOVERNMENT OF INDIA) INSTITUTION'S INNOVATION COUNCIL

Highest Star Status by the MoE, Govt. of India for Innovation and Entrepreneurship Activities



 **Google Meet**

 **10/10/2025**  
10.45 AM TO 12.15 PM

**CONVENORS**

**Dr. J. Reegan**  
HOD/ECE

**Dr. J. Jasper Gnanachandran**  
Principal

Visit us @ [www.mtec.ac.in](http://www.mtec.ac.in)

Coordinators  
**Mrs R Kavitha , AP/ECE**  
**Mrs K Malathy , AP/ECE**

 /SMTENGG

## STUDENTS' ACHIEVEMENTS



**SPORTS**  
ChettinadTech




**Singles**

**M. B. Gokulnath | II- IT 1st Place**  
**V .Dhivakar | II- AI&DS**  
&  
**M.Kaviya | II- MBA**  
**3rd Place**

**Doubles**

**D.Vikneshwaran | III - ECE,**  
**M. B. Gokulnath | II - IT**  
**3rd Place**  
**M. Kaviya | II - MBA,**  
**S. Menaga | II - CSE**  
**2nd Place**

***Congratulations***  
ChettinadTech **Badminton** Team  
for being the Prize winners of the  
**CM Trophy 2025**



ChettinadTech Sports

 **ChettinadTech.ac.in**

**PLACEMENTS**

We are delighted to announce the successful placement of our **ECE students**.

The Management, Principal, Faculty, and Placement Cell of **Chettinad College of Engineering & Technology** congratulate the students on their achievement and wish them a bright career ahead.



**Hearty Congratulations!**

We Congratulate our **BE - ECE** students placed as **VLSI Design Engineer** in

**TESSOLVE**

Salary Package : 4.2 LPA



**DHARANI K**      **VEENA R**

NH - 67, Karur - Trichy Highway, Puliur CF, Karur - 639114. [chettinadtech.ac.in](http://chettinadtech.ac.in)



**CONGRATULATIONS!**

We congratulate our final year **BE Students** placed as **Associate Trainer (Technical / Aptitude)** in

**FACE Prep**      **SALARY : 3 LPA**



[Chettinadtech.ac.in](http://Chettinadtech.ac.in)



**CONGRATULATIONS!**

We Congratulate our **BE Student** placed as **Trainee Engineer** in

**AVASOFT**  
SHAPING NEXT GEN

**SALARY : 6 LPA**



**PRADEEP C**  
IV ECE

[chettinadtech.ac.in](http://chettinadtech.ac.in)



**CONGRATULATIONS!**

We congratulate our **BE Student** placed as **Technical Mentor (Data Structures & Algorithms)** in

**FACE Prep**

**SALARY PACKAGE : 5 LPA**



**PRADEEP C**  
IV ECE

NH - 67, Karur - Trichy Highway, Puliur CF, Karur - 639114. [chettinadtech.ac.in](http://chettinadtech.ac.in)



**Chettinad**

**College of Engineering & Technology**

Approved by AICTE-New Delhi and Affiliated to Anna University-Chennai.

NH-67, Karur-Trichy Highway, Puliur CF, Karur, Tamil Nadu – 639114.

☎ 93450 02630, 93607 02630

✉ principalccet@chettinadtech.ac.in

🌐 [www.chettinadtech.ac.in](http://www.chettinadtech.ac.in)