

CIRCUITRY

N E W S L E T T E R

ELECTRICAL & ELECTRONICS ENGINEERING



April - May 2022

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VOLUME
NO. 3
(Issue 2)

CREATIVE DESK

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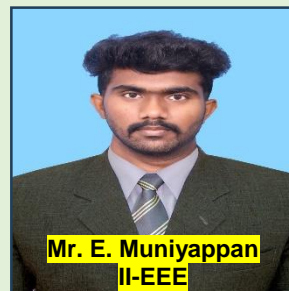



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CIRCUITRY CHRONICLES

NEWSLETTER

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

About the Department

The Department of Electrical and Electronics was established in the year 2008 with the aim of combining modern teaching methods with inter-disciplinary knowledge, human values and professional ethics. The department offers a unique blend of theory and practice. It provides a quality learning environment, in terms of state-of-the-art facilities, sharing and widening of knowledge through MoU with relevant industries and interacting with experts from academia and industry. The department is well equipped with state-of-the-art laboratories such as the Electrical Machines Lab, Electric Circuits Lab, Control Systems Lab, Measurement and Instrumentation Lab, Engineering Practices Lab, Power Electronics Lab, Power System Simulation Lab and Electric Drives and Control Lab. To improve practical and simulation skills, MATLAB Software with all tool boxes has been provided. Power World Simulator, MI Power, PSpice and MultiSim software programmes have been provided to improve the designing ability of the students.

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NEWSLETTER

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

VISION

- To create a thriving community where enduring student relationships flourish, fostering a culture of innovative idea development, socially responsible, and ethically driven engineers in the electrical industry.

MISSION

- To nurture students, enabling them to effectively confront professional challenges and emerge as outstanding engineers and technocrats.
- To provide a holistic and comprehensive education that ensures total quality, encompassing broad exposure and value additions.
- To engage in research within the realm of Electrical and Electronics Engineering, addressing the needs of the industry, scientific community, and society at large.

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Programme Name: Movie Time (Children of Heaven)

Date: 11-04-2022

Class & No. of Participants: II Year-IV Year & 78 students

Venue: C Block Seminar Hall

Description:

On 11-04-2022, the Department of Electrical and Electronics Engineering organised a screening of the film "Children of Heaven" for II, III, and IV year B.E-EEE students. Our college has begun to do its part to provide our future generations with good, feel-good learning through movies. As everyone has fond memories of their college days, when they spent time together, and movies have become everyone's favourite hobby, we made this arrangement to encourage our students and play our part in spreading joy. All the students internalized the good messages presented in the movie. "Children of Heaven" brilliantly demonstrates the sweet story of a brother and sister, their bond and their unique solution to solving the problem of a pair of lost shoes. This film manages to keep the students constantly engrossed in its action without once resorting to even a smidgen of incredibility or endurance. Finally, Ms. D. Shobana Reni, III-EEE shared her movie reflection. She shared "Children of Heaven" is cute, amiable, forgettable entertainment that evokes memories of Vittorio De Sica's "Bicycle Thieves" and Albert Lamorisse's "The Red Balloon". It was such an enjoyable powerful moral story. It has just the right amount of charm from the child actors.

Event Photos:



Programme Name: Alumni Talk on “Design of Electrical Layout”

Date: 13-04-2022

Class & No. of Participants: II Year-IV Year & 80 students

Venue: C Block Seminar Hall

Description:

On 13-04-2022, the Department of Electrical and Electronics Engineering organised an Alumni Talk Programme on “Design of Electrical Layout” for all ChettinadTech B.E-EEE students. D. Shobana Reni, 3rd year EEE, greeted and welcomed all the participants. Mr. P. Pandi, SAP/EEE introduced the chief guest to our participants. Mrs. A. Bhuvanewari, SAP/EEE honoured the chief guest with a gift. The Chief Guest explained, the electrical plan is sometimes called an electrical drawing or wiring diagram. It is a type of technical drawing that delivers visual representation and describes circuits and electrical systems. It consists of electrical symbols and lines that showcase the engineer's electrical design to its clients. He also explained how to create house or industrial plan specification, Schedule of loads, Electrical project estimation and needed software to create site plans. Finally, he shared his working experience and opportunity of electrical designing in various sectors to our students. Some students asked questions and shared their reflections about the session. Mr. N. Vijayasarithi, HOD/EEE, concluded the programme with a vote of thanks.

Event Photos:



Programme Name: Inter Level Contest on “Disenyo PCB Challenge’22”

Date: 25-04-2022

Coordinator: Mr. M. Vasanthprakash, AP/EEE

No. of Participants: 54 Students

Venue: EEE Workshop

Description:

On 25-04-2022, the Department of Electrical and Electronics Engineering organized an Inter Level PCB Contest on “DISENYO PCB CHALLENGE’22” for all ChettinadTech B.E students. The contest was divided into three rounds and students were selected. Students from various disciplines registered, and selected participants showcased their skills. A total of 66 students registered as two students per group. Finally, 54 students of 27 group were selected to showcase their PCB design skill. In this contest, the students submitted their PCB design work to juries to evaluate Electronic voting machine, Automatic light control, Water pump control with level indicator, turning accident preventer, and many more. Our internal faculty members of ChettinadTech Mr. P. Pandi, AP/EEE and Mr. P. Selvan, AP/ECE evaluated students’ PCB design. The principal of our college, Dr. A. Punitha honored the best PCB design. The 1st prize was awarded to Ms. D. Shobana Reni-III EEE, and Ms. S. Induma-III EEE. The 2nd prize was awarded to Ms. D. Emilin Pearl Sharal-III ECE, and Ms. C. Dhanushiya-III ECE. The 3rd prize was awarded to Mr. V. Surya-I CSE, and Mr. S. V. Muthu Annamalai-I CSE. Mr. N. Vijayasarithi, HOD/EEE, thanked all the students who participated in this contest.

Event Photos:





Programme Name: Inter Level Contest on “Create an Electrical Layout”

Date: 13-05-2022

Coordinator: Ms. P. Thenmozhi, AP/EEE

No. of Participants: 46 Students

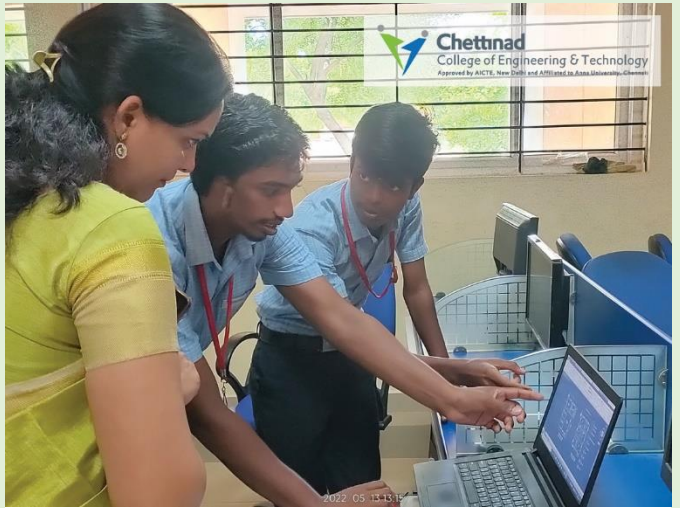
Venue: G Block EEE Computer Lab

Description:

The Department of Electrical and Electronics Engineering organised a contest “Create an Electrical Layout” for EEE Students on 13-05-2022. A total of 46 students registered as two students per group. In this contest, the students designed the electrical layout diagram with the help of AUTOCAD software. Students explained their electrical layout diagram to the juries. Our internal faculty members of Chettinad College of Engineering and Technology Mr. V. Hariprasath, HOD/Mech and Dr. K. P. Senthilnathan, HOD/Civil evaluated student’s Electrical Layout design. They evaluated based on criteria such as, Presentation, Chart Work, Designing in AUTOCAD, Creativity, Application and clarity of presentation. The 1st prize was awarded to Mr. P. Muthukrishnan-III EEE, and Mr. P. Selvakumar -III EEE. The 2nd prize was awarded to Mr. G. Surendhar-III EEE, and Mr. V. Ponshankar -III EEE. The 3rd prize was awarded to Mr. P. Senthilmurugan-II EEE, and Mr. G. Bhuvanesh-II EEE. Mr. N. Vijayasarithi, HOD/EEE, thanked all the students who participated in this contest.

Event Photos:







INSTITUTION INNOVATION COUNCIL

Chettinad Group

The Chettinad Group has over 90 years of experience facilitating a wide range of educational and service institutions successfully in Tamil Nadu. The group is currently responsible for 22 private and government-aided schools, Polytechnic Medical, Dentistry, Pharmaceutical, Engineering, Law and Architecture colleges.



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