

CIRCUITRY CHRONICLES

N E W S L E T T E R

ELECTRICAL & ELECTRONICS ENGINEERING



JANUARY - FEBRUARY 2026

CONTACT US

CHETTINAD COLLEGE OF ENGINEERING & TECHNOLOGY,
NH-67, KARUR-TRICHY HIGHWAY,
PULIYUR C.F, KARUR.

PIN CODE: 639114

Visit us: www.chettinadtech.ac.in

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CREATIVE DESK

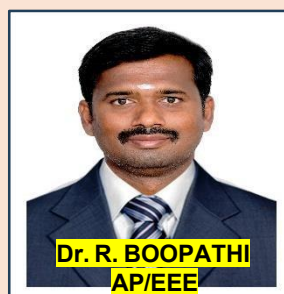
Advisory Board



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Student Editors



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

About the Department

The Department of Electrical and Electronics Engineering was established in the year 2008 with the aim of combining modern teaching methods with interdisciplinary knowledge, human values and professional ethics. The department offers a unique blend of theory and practice. It provides a quality learning environment, in terms of state-of-the-art facilities, sharing and widening of knowledge through Memorandum of Understanding (MoU) with relevant industries and interacting with experts from academia and industry. The department is well equipped with state-of-the-art laboratories such as the Electrical Machines Lab, Electric Circuits Lab, Control Systems Lab, Measurement and Instrumentation Lab, Engineering Practices Lab, Power Electronics Lab, Power System Simulation Lab and Electric Drives and Control Lab. To improve practical and simulation skills, MATLAB Software with all tool boxes has been provided. Power World Simulator, MI Power, PSpice and MultiSim software programs have been provided to improve the designing ability of the students.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

VISION

- To create a thriving community where enduring student relationships flourish, fostering a culture of innovative idea development, socially responsible, and ethically driven engineers in the electrical industry.

MISSION

- To nurture students, enabling them to effectively confront professional challenges and emerge as outstanding engineers and technocrats.
- To provide a holistic and comprehensive education that ensures total quality, encompassing broad exposure and value additions.
- To engage in research within the realm of Electrical and Electronics Engineering, addressing the needs of the industry, scientific community, and society at large.

Programme Name: Value-Added Course on “Simulation of Electrical Systems using MATLAB”

Target Group: II Year B.E. Electrical and Electronics Engineering (EEE) Students

Time: 9.00AM to 4.15 PM

Date: 05.01.2026 – 06.01.2026

Organized by: Association of Electrical and Electronics Engineering – ELECTRA & IEI Student Chapter

Resource Persons: Dr. M. Senthil Kumar, Prof./EEE, Dr. R. Boopathi, AP/EEE, Mrs. P. Thenmozhi, AP/EEE

Venue: EEE Computer Lab(G04)

Description:

A Value-Added Course on “Simulation of Electrical Systems Using MATLAB” was organized by the Department of Electrical and Electronics Engineering for second-year EEE students from January 5 to January 6, 2026. The course sessions were led by our department faculty members Dr. M. Senthil Kumar, Prof./EEE, Dr. R. Boopathi, AP/EEE, and Mrs. P. Thenmozhi, AP/EEE faculty members of the Department of Electrical and Electronics Engineering. The program primarily focused on introducing students to the fundamentals of MATLAB and Simulink for electrical system simulation.

The course emphasized the modeling, analysis, and performance evaluation of electrical and electronic systems using MATLAB and Simulink environments. Mathematical models of electrical systems were developed in MATLAB based on governing equations, while Simulink was used to represent these models through functional blocks such as sources, gains, integrators, and scopes. This approach enabled effective time-domain analysis and visualization of system responses.

The value-added course enhanced students’ understanding of theoretical concepts through practical simulation, improved their analytical skills, and provided hands-on experience in using MATLAB tools for solving real-world electrical engineering problems.

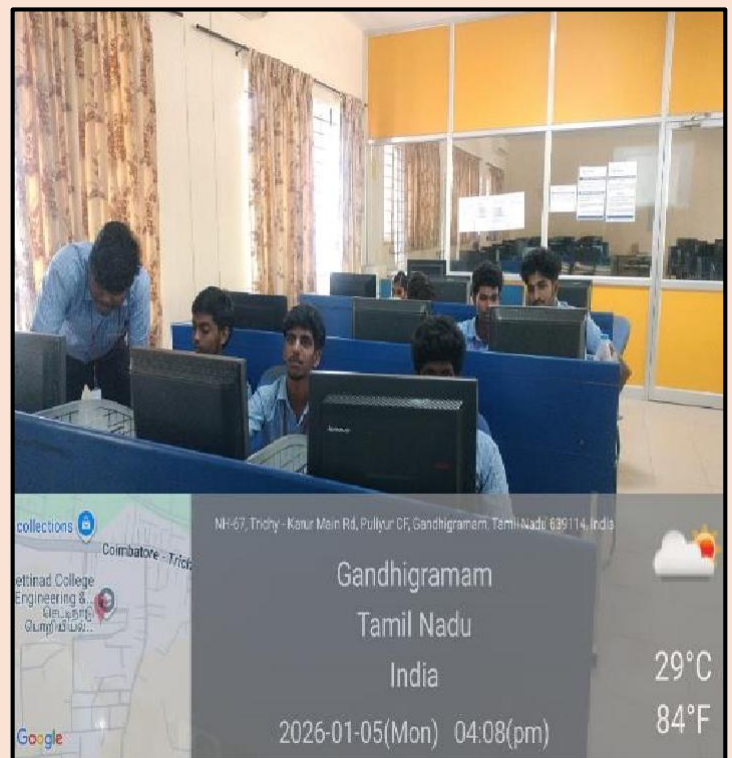
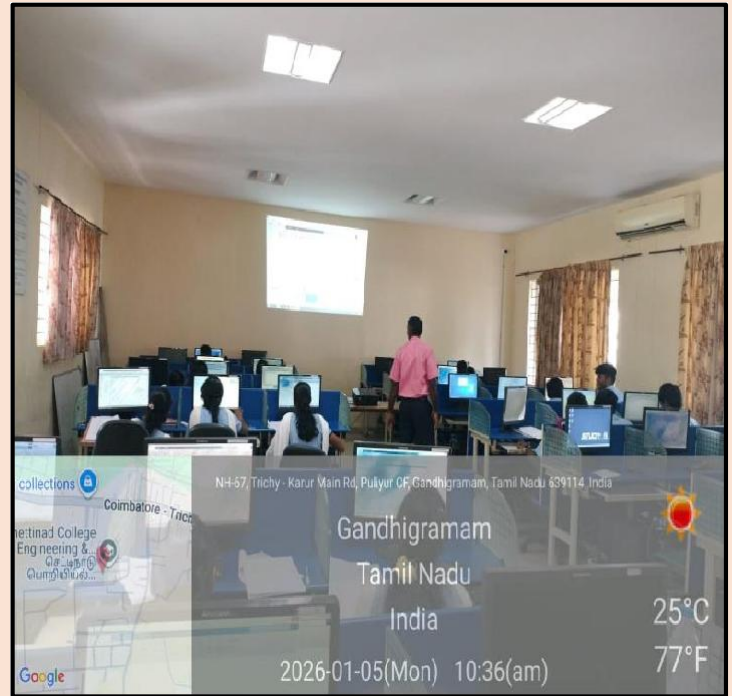
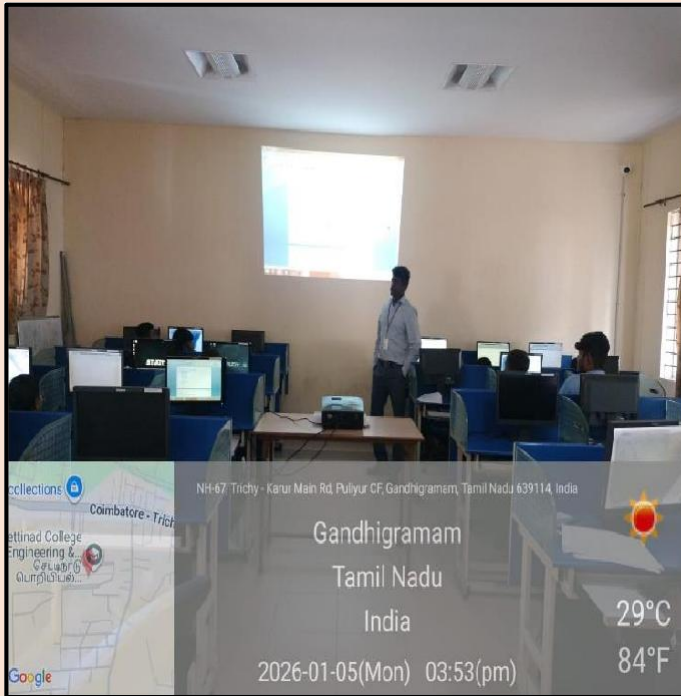
SDG Mapping:

The Value-Added Course on Simulation of Electrical Systems Using MATLAB contributes to the following United Nations Sustainable Development Goals (SDGs) by promoting technical education, innovation, and sustainable engineering practices.

SDG 4 – Quality Education: Enhances students’ technical competency through skill-based learning. Promotes practical, industry-relevant education using modern simulation tools.

SDG 9 – Industry, Innovation, and Infrastructure: Encourages the use of modern engineering tools (MATLAB & Simulink).

Value-Added Course Photos:



Programme Name: Technical Activity on “Simulation of power system faults and relay coordination”

Target Group: III Year B.E. Electrical and Electronics Engineering (EEE) Students

Date: 22.01.2026

Activity Incharge: Dr. M. Senthil Kumar, Prof./EEE

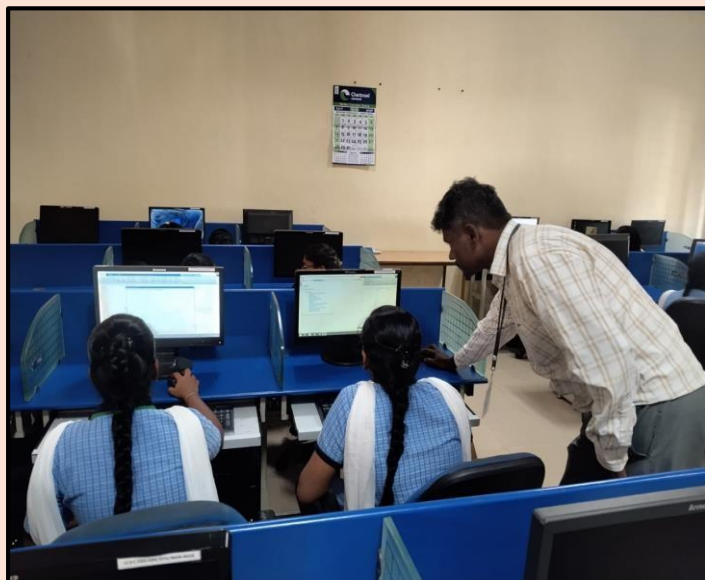
Venue: EEE Computer Lab(G04)

Description:

The Department of Electrical and Electronics Engineering organized a Technical Activity on "Simulation of Power System Faults and Relay Coordination" for III Year EEE students on 22 January 2026 at the EEE Computer Laboratory. The activity provided students with hands-on experience in simulating various power system faults, including single line-to-ground, line-to-line, double line-to-ground, and three-phase faults, using simulation software

Students also learned the principles of protective relay coordination by analyzing fault conditions and configuring relay settings for effective fault isolation. The session enhanced their practical understanding of power system protection, improved analytical and simulation skills, and strengthened their ability to interpret fault analysis results. A total of 31 students actively participated in the activity, demonstrating excellent involvement and technical competence.

Activity Photos:



Programme Name: Peer Learning on “Energy Audit and Energy Conservation”

Target Group: II & III Year B.E. Electrical and Electronics Engineering (EEE) Students

Date: 23.01.2026

Venue: G Block Seminar Hall

Description:

The Department of Electrical and Electronics Engineering organized a Peer Learning Session on 23 January 2026 at 3:30 PM in the C Block Seminar Hall. The session was conducted by Karthikeyan P and Rohith R, III Year EEE students, on the topic "Energy Audit and Energy Conservation." The presenters shared their knowledge on the significance of energy auditing, methods for identifying energy losses, and practical energy conservation techniques in residential, commercial, and industrial sectors.

The interactive session encouraged active participation, collaborative learning, and technical discussions among students. It enhanced awareness of sustainable energy management practices while strengthening communication, presentation, and peer-to-peer learning skills. The program successfully promoted a culture of knowledge sharing and inspired students to adopt energy-efficient solutions in engineering applications.



The poster is for a 'PEER LEARNING' session. At the top left is the Chettinad College of Engineering & Technology logo, with text 'Approved by AICTE-New Delhi and Affiliated to Anna University-Chennai'. To the right are logos for '4 SKILLS EDUCATION', '10 SUSTAINABLE DEVELOPMENT GOALS', 'INSTITUTION'S INNOVATION COUNCIL', 'R World Institutional RANKING', and a circular award logo. The main title 'PEER LEARNING' is in large yellow and white letters. Below it, a clock icon shows '03.30 PM' and a calendar icon shows '23 JANUARY 2026'. A yellow banner states 'Venue : C Block Seminar Hall'. Below this is the text 'Let's Connect & Share with PEER'. Two circular portraits of the speakers are shown: Karthikeyan P (left) and Rohith R (right). Below each portrait is a yellow box with their names and 'III EEE'. The title of the session, 'Energy Audit and Energy Conservation', is centered below the speakers. At the bottom, the website 'WWW.CHETTINADTECH.AC.IN' and social media icons for Facebook, Twitter, and Instagram are displayed.

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4 SKILLS EDUCATION 10 SUSTAINABLE DEVELOPMENT GOALS

INSTITUTION'S INNOVATION COUNCIL

R World Institutional RANKING

PEER LEARNING

03.30 PM 23 JANUARY 2026

Venue : C Block Seminar Hall

Let's Connect & Share with PEER

KARTHIKEYAN P
III EEE

ROHITH R
III EEE

Title :
Energy Audit and Energy Conservation

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f t i

Programme Name: Technical Activity on “Load frequency control using MATLAB”

Target Group: III Year B.E. Electrical and Electronics Engineering (EEE) Students

Date: 29.01.2026

Activity Incharge: Mrs. P. Thenmozhi, AP/EEE

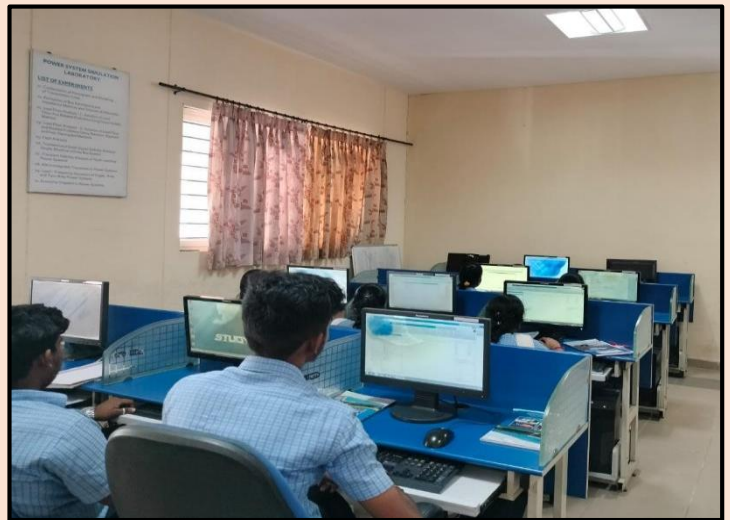
Venue: EEE Computer Lab(G04)

Description:

The Department of Electrical and Electronics Engineering organized a Technical Activity on "Load Frequency Control using MATLAB" for III Year EEE students on 29 January 2026 at the G Block Computer Laboratory (G04). The activity provided practical exposure to the concept of Load Frequency Control (LFC) through MATLAB/Simulink simulations. Students developed a single-area LFC model by integrating governor, turbine, and generator blocks and analyzed system performance under varying load disturbances.

They observed important response characteristics such as frequency deviation, settling time, and steady-state error, gaining a deeper understanding of power system stability and control. The hands-on session enhanced students' simulation skills, analytical thinking, and confidence in using MATLAB for real-time power system studies. A total of 33 students actively participated, making the activity highly interactive and outcome-oriented.

Activity Photos:



Proud Moment for the Department of EEE!

Our department third year students Karthikeyan P, Mythili G, Shamyuktha R, Sureka S & Rohith R from Chettinad College of Engineering and Technology, Karur have secured Third Place in the National Level Energy Audit Competition conducted during Energy Conservation Week, hosted by Vellore Institute of Technology (VIT), Vellore on 10-01-2026. A remarkable achievement showcasing technical excellence, teamwork, and commitment towards energy conservation and sustainability.

Congratulations to the team!



Department Placement

**Chettinad**

College of Engineering & Technology

Approved by AICTE-New Delhi and Anna University-Chennai



Department of Training and Placement

Congratulations!

We congratulate our 20 final year B.E. students on their placements as **Junior Testing Engineer** at **Etricals Engineers Private Limited**



TO LEAD THE LEADERS

Salary : 3 LPA

7 students from ECE & 13 Students from EEE

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CHETTINADTECH - WHERE LEARNING BECOMES JOYFUL

- Curriculum Integrated Placement Training
- TBI - Technology Business Incubator
- IBM and Infosys Training
- Foreign Language Training
- 100% Placement Assistance
- International Placements
- Skill Based Courses
- WiFi Smart Classrooms



Students Placement - Average Salary 3.8 LPA



MERIT SCHOLARSHIPS*

B.E., & B.TECH.

Cut off	170+	165+	160+	155+
Scholarship	100%	75%	50%	25%

LATERAL ENTRY

Cut off	90+	80+	75+
Scholarship	100%	75%	50%

Source: Data

TRAINING & PLACEMENTS



Prominent Recruiters



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4700+

Alumni Network

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Average Salary Package

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