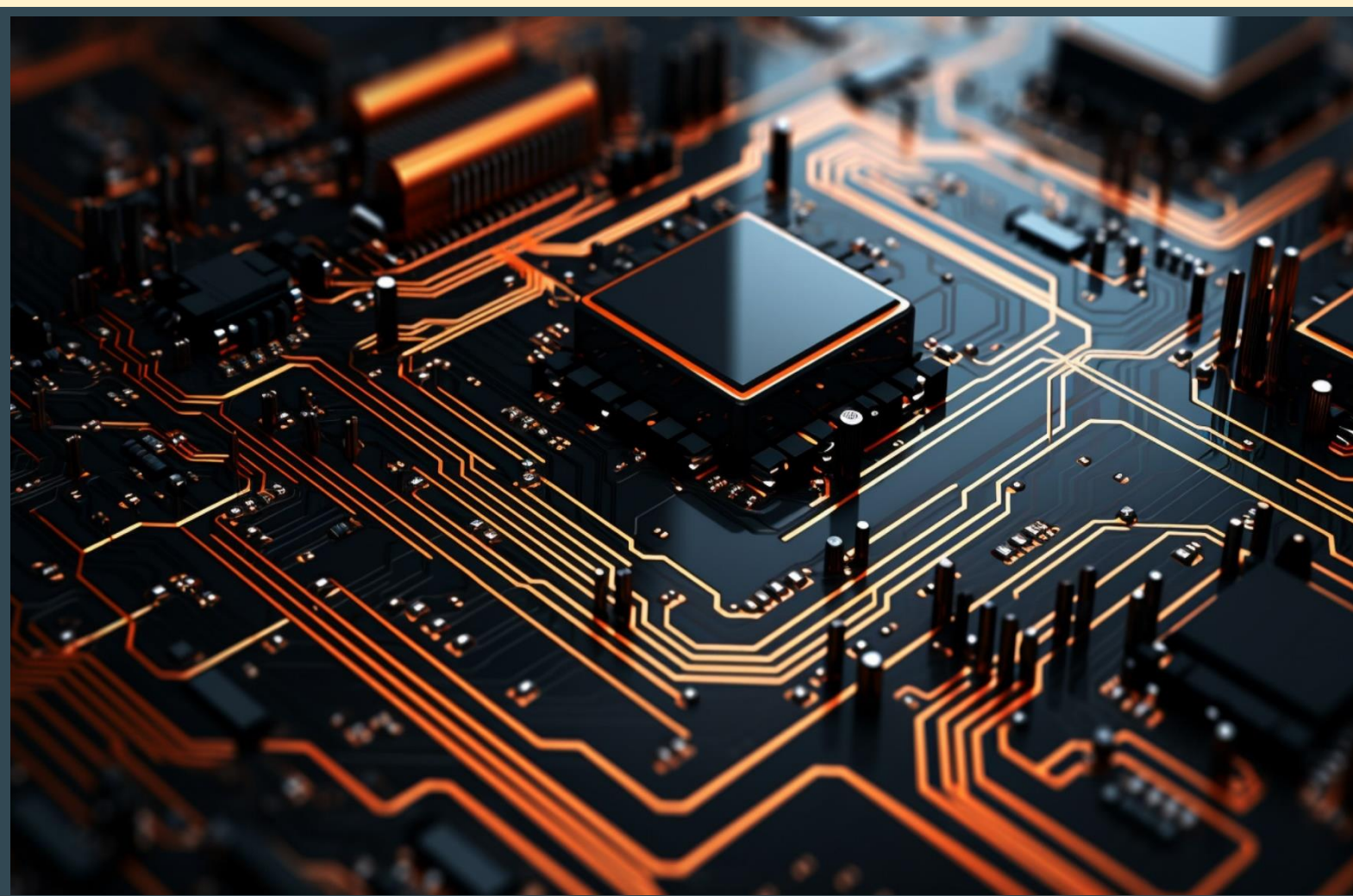


CIRCUITRY CHRONICLES

N E W S L E T T E R

ELECTRICAL & ELECTRONICS ENGINEERING



J U L Y - A U G U S T 2 0 2 5

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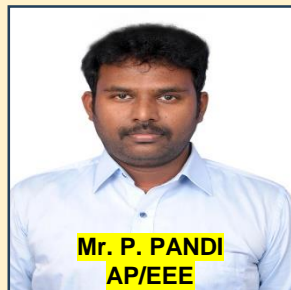
Advisory Board



Editor-in- Chief



Board of Editors



Student Editors



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

About the Department

The Department of Electrical and Electronics Engineering was established in the year 2008 with the aim of combining modern teaching methods with interdisciplinary knowledge, human values and professional ethics. The department offers a unique blend of theory and practice. It provides a quality learning environment, in terms of state-of-the-art facilities, sharing and widening of knowledge through Memorandum of Understanding (MoU) with relevant industries and interacting with experts from academia and industry. The department is well equipped with state-of-the-art laboratories such as the Electrical Machines Lab, Electric Circuits Lab, Control Systems Lab, Measurement and Instrumentation Lab, Engineering Practices Lab, Power Electronics Lab, Power System Simulation Lab and Electric Drives and Control Lab. To improve practical and simulation skills, MATLAB Software with all tool boxes has been provided. Power World Simulator, MI Power, PSpice and MultiSim software programs have been provided to improve the designing ability of the students.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

VISION

- To create a thriving community where enduring student relationships flourish, fostering a culture of innovative idea development, socially responsible, and ethically driven engineers in the electrical industry.

MISSION

- To nurture students, enabling them to effectively confront professional challenges and emerge as outstanding engineers and technocrats.
- To provide a holistic and comprehensive education that ensures total quality, encompassing broad exposure and value additions.
- To engage in research within the realm of Electrical and Electronics Engineering, addressing the needs of the industry, scientific community, and society at large.

Programme Name: PLC and SCADA Value-added Certification Course

Date: 06-08-2025 to 08-08-2025

Class & No. of Participants: III EEE & 35

Venue: G Block EEE Computer Lab

External Trainer Name: Ms. Madhu Bala, Embedded and PLC Developer

Description:

The Department of Electrical and Electronics Engineering organized a Value-Added Certification Course on PLC and SCADA from 06th to 08th August 2025 for the 3rd year EEE students. The sessions were handled by Ms. Madhu Bala, Embedded and PLC Developer, at the G Block EEE Computer Lab, with 35 students actively participating. The programme aimed to enhance students practical skills in industrial automation, combining theory with real-time applications.

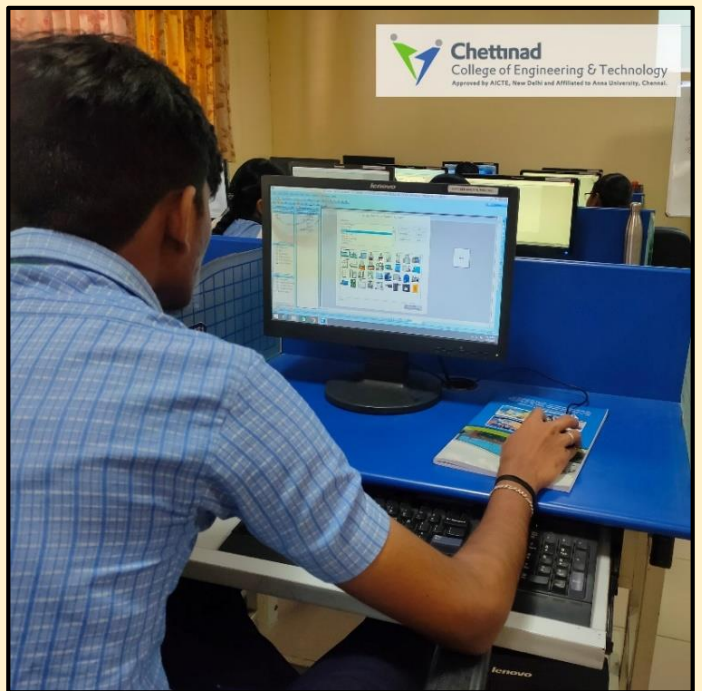
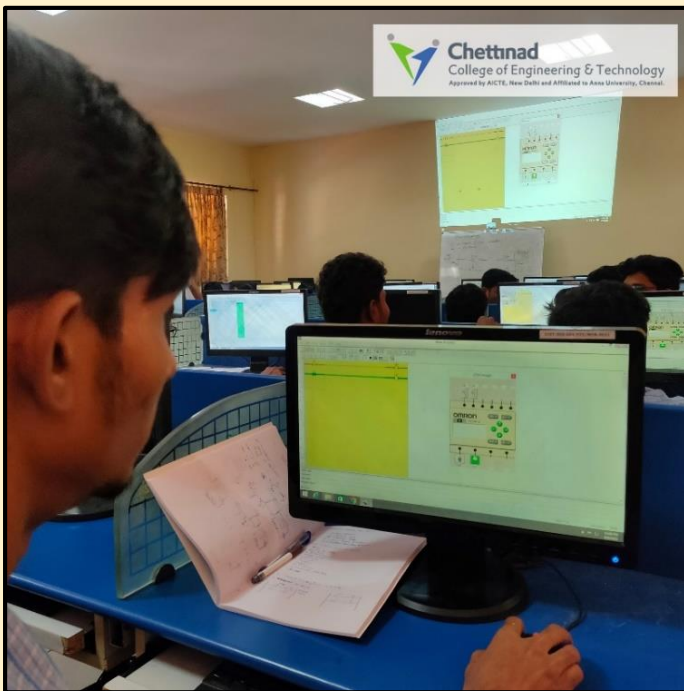
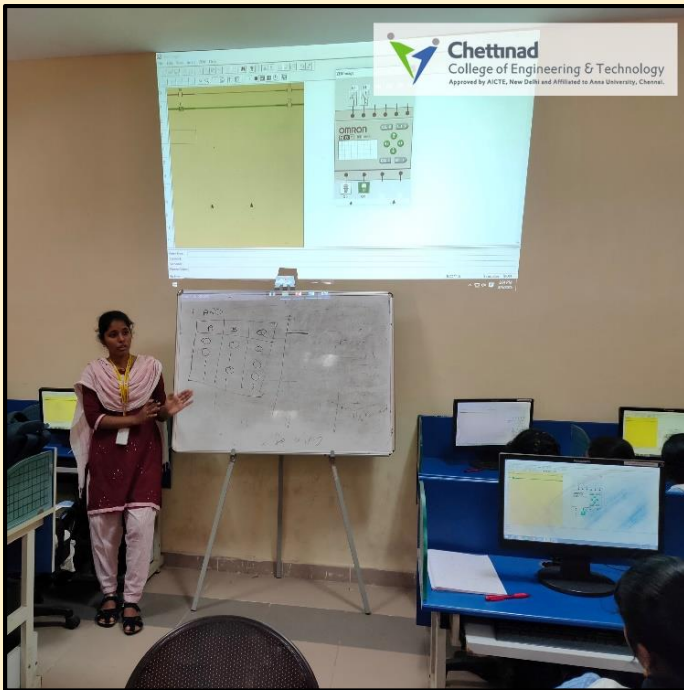
Day 1 began with an introduction to Programmable Logic Controllers (PLCs), including pin diagram explanation, relay logic operations using Normally Open (NO) and Normally Closed (NC) contacts, and PLC software installation. Students learned to create sample ladder logic programs, understand latching and memory coils, and work with different timers and counters. The day also included simulation projects such as School Bell, Car Parking, Snacks Vending Machine, Power Bank Task, and Warehouse Lamp Control System.

Day 2 introduced Supervisory Control and Data Acquisition (SCADA) systems, covering SCADA basics and an overview of Wonderware InTouch. Students practiced tag creation, menu and wizard functions, graphic designing, input/output mapping, symbol factory usage, page navigation, location movement, and percentage fill features. Practical SCADA projects included Tank Level Control and Automatic Bottle Filling.

Day 3 focused on advanced application design. Students learned to create real-time and historical trends, design application alarms, develop PLC-based control systems, and integrate them with OMRON PLCs for testing and execution.

By the end of the three-day programme, students were able to understand automation workflows, troubleshoot PLC systems, and design basic SCADA screens. The training enriched their technical competency and prepared them for future opportunities in the automation and manufacturing sectors.

Event Photos:



Program Name: IEI – All India Seminar on “Green India Mission” at ChettinadTech

Date: 22-08-2025 to 23-08-2025

Number of Participants: 121

Venue: Chettinad College of Engineering and Technology, Karur

Description:

The Institution of Engineers (India), in collaboration with the Department of Electrical and Electronics Engineering, Chettinad College of Engineering and Technology, Karur, organized a two-day All India Seminar on “Green India Mission” on 22nd and 23rd August 2025. The event brought together experts, academicians, and students to deliberate on sustainable development and environmental conservation.

The seminar was inaugurated on 22nd August. Dr. S. Dharmalingam, National Council Member, IEI (India), welcomed the gathering and highlighted the seminar theme, emphasizing the National Action Plan on Climate Change and the need to expand forest and tree cover.

The Chief Guest, Dr. R. Elankovan, Additional General Manager & Head–HSE, BHEL Tiruchirappalli, in his address, outlined the aims of the Green India Mission. He stressed initiatives such as the Green Credit Programme, Harit BHEL, and Miyawaki forests, urging participants to take a pledge towards a greener future.

Delivering the Presidential Address, Dr. A. Punitha, Principal of ChettinadTech, emphasized the relevance of the UN Sustainable Development Goals (SDGs) and highlighted the green energy activities initiated at ChettinadTech.

As part of the seminar, students actively participated in Paper and Poster Presentations. A total of 73 students from ChettinadTech and 48 students from external colleges showcased innovative ideas related to renewable energy and environmental sustainability.

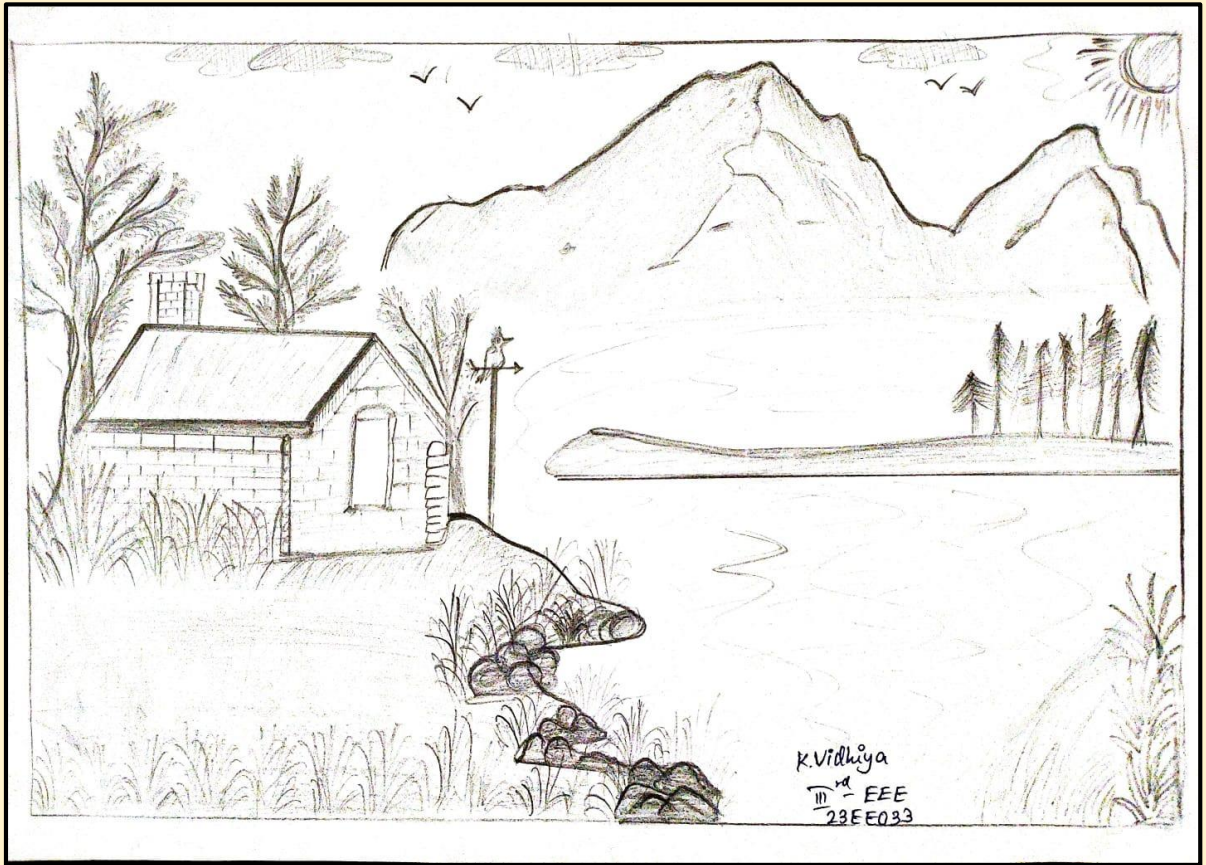
The Valedictory Function on 23rd August featured Dr. S. Dharmalingam as the Chief Guest. Mr. P. Pandi, Assistant Professor/EEE and Coordinator, presented the seminar report, summarizing the two-day proceedings. Dr. M. Senthilkumar, Professor/EEE, proposed the Vote of Thanks, expressing gratitude to all contributors. The event concluded with the distribution of prizes and certificates to the winners of Paper and Poster Presentation competitions.

The two-day seminar brought together academicians, industry experts, researchers, and students, who deliberated on green energy practices, afforestation strategies, carbon neutrality, and sustainable development. The event concluded with a collective commitment to continue Green India initiatives and to strengthen the role of engineers in addressing environmental challenges. The seminar served as a remarkable platform for creating awareness, sharing knowledge, and motivating young minds to contribute towards a sustainable and green India.



STUDENT ART BEAT







INSTITUTION INNOVATION COUNCIL

Chettinad Group

The Chettinad Group has over 90 years of experience facilitating a wide range of educational and service institutions successfully in Tamil Nadu. The group is currently responsible for 22 private and government-aided schools, Polytechnic Medical, Dentistry, Pharmaceutical, Engineering, Law and Architecture colleges.

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LIFE AT ChettinadTech



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