

# CIRCUITRY

N E W S L E T T E R

ELECTRICAL & ELECTRONICS ENGINEERING



May - June 2025

## CONTACT US

CHETTINAD COLLEGE OF ENGINEERING & TECHNOLOGY,  
NH-67, KARUR-TRICHY HIGHWAY,  
PULIYUR C.F, KARUR.  
PIN CODE: 639114  
URL: [www.chettinadtech.ac.in](http://www.chettinadtech.ac.in)

**VOLUME**  
**NO. 6**  
(Issue 3)

# CREATIVE DESK

---

## Advisory Board

---



---

## Editor-in-Chief

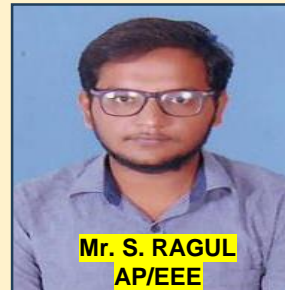
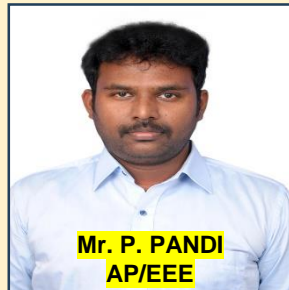
---



---

## Board of Editors

---



---

## Student Editors

---



# Table of Contents

	1	About the Department
Department Vision & Mission	2	
	3	Batch 14-Farewell Day
Prestigious Grant from TNSCST	4	

### **DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

#### **About the Department**

The Department of Electrical and Electronics Engineering was established in the year 2008 with the aim of combining modern teaching methods with inter-disciplinary knowledge, human values and professional ethics. The department offers a unique blend of theory and practice. It provides a quality learning environment, in terms of state-of-the-art facilities, sharing and widening of knowledge through MoU with relevant industries and interacting with experts from academia and industry. The department is well equipped with state-of-the-art laboratories such as the Electrical Machines Lab, Electric Circuits Lab, Control Systems Lab, Measurement and Instrumentation Lab, Engineering Practices Lab, Power Electronics Lab, Power System Simulation Lab and Electric Drives and Control Lab. To improve practical and simulation skills, MATLAB Software with all tool boxes has been provided. Power World Simulator, MI Power, PSpice and MultiSim software programmes have been provided to improve the designing ability of the students.

### **DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

#### **VISION**

- To create a thriving community where enduring student relationships flourish, fostering a culture of innovative idea development, socially responsible, and ethically driven engineers in the electrical industry.

#### **MISSION**

- To nurture students, enabling them to effectively confront professional challenges and emerge as outstanding engineers and technocrats.
- To provide a holistic and comprehensive education that ensures total quality, encompassing broad exposure and value additions.
- To engage in research within the realm of Electrical and Electronics Engineering, addressing the needs of the industry, scientific community, and society at large.

**Programme Name:** Batch 14- Farewell Day

**Date:** 23-05-2025

**No. of participants:** 42

**Venue:** G Block Seminar Hall

Description:

The Department of Electrical and Electronics Engineering organized a heartfelt Farewell Day Function for Batch 14 on 23rd May 2025 at the G Block Seminar Hall, CCET. The event was a tribute to the achievements, contributions, and cherished memories of the outgoing students, held in the presence of their juniors and faculty members.

The program commenced with the traditional Tamil Thai Vazhthu rendered by the III year EEE girls, setting a respectful and solemn tone. This was followed by a warm welcome speech delivered by Pratheesha Vardhini K L – III EEE, who expressed admiration, love, and appreciation for the seniors. A representative from Batch 14 then delivered the farewell address, reflecting on their four-year journey filled with learning, laughter, and lasting friendships, while expressing heartfelt gratitude to the faculty and peers.

The highlight of the day was a vibrant surprise performance by the juniors, featuring energetic dance, soulful music, and humorous skits, which brought delight and excitement to the audience. The mood then turned nostalgic as juniors presented tokens of appreciation to the seniors, followed by emotional memory-sharing moments from selected outgoing students.

The faculty members offered inspiring and heartfelt farewell messages, blessing the students for a successful future and encouraging them to remain connected to their roots. A touching farewell song performed by the juniors added an emotional close to the event.

The program concluded with a sincere vote of thanks delivered by Nageshwari P – III EEE, followed by a group photo session that captured the spirit of celebration, togetherness, and farewell.

## Farewell Day



## **Prestigious Grant from TNSCST**

We are proud to announce that the Department of Electrical and Electronics Engineering has secured a prestigious Student Project Scheme grant for the academic year 2024–2025 from the Tamil Nadu State Council for Science and Technology (TNSCST).

### **Project Title:**

“Design of Single Input Multi Output Interleaved Boost Converter for EV Mobile Battery Charge”

### **Team Members:**

1. Mr. Giri Prasath S
2. Mr. Lakshmikanth L
3. Mr. Karthikraja P
4. Mr. Karuppasamy B

### **Mentor:**

Dr. M. Senthilkumar, Professor / EEE



The team has been awarded a **grant of ₹7,500/-** to support their innovative project, which aims to contribute practical and energy-efficient solutions in the field of electric vehicle charging systems.

Hearty congratulations to the students and their mentor for this remarkable achievement and their commitment to developing impactful, real-world engineering solutions!



INSTITUTION INNOVATION COUNCIL

## Chettinad Group

The Chettinad Group has over 90 years of experience facilitating a wide range of educational and service institutions successfully in Tamil Nadu. The group is currently responsible for 22 private and government-aided schools, Polytechnic Medical, Dentistry, Pharmaceutical, Engineering, Law and Architecture colleges.



## LIFE AT ChettinadTech



**Chettinad**

College of Engineering & Technology

NH - 67, Karur Trichy Highway, Puliur CF, Karur - 639 114.  
Tamilnadu, India.

93450 02630 93607 02630

info@chettinadtech.ac.in / www.chettinadtech.ac.in