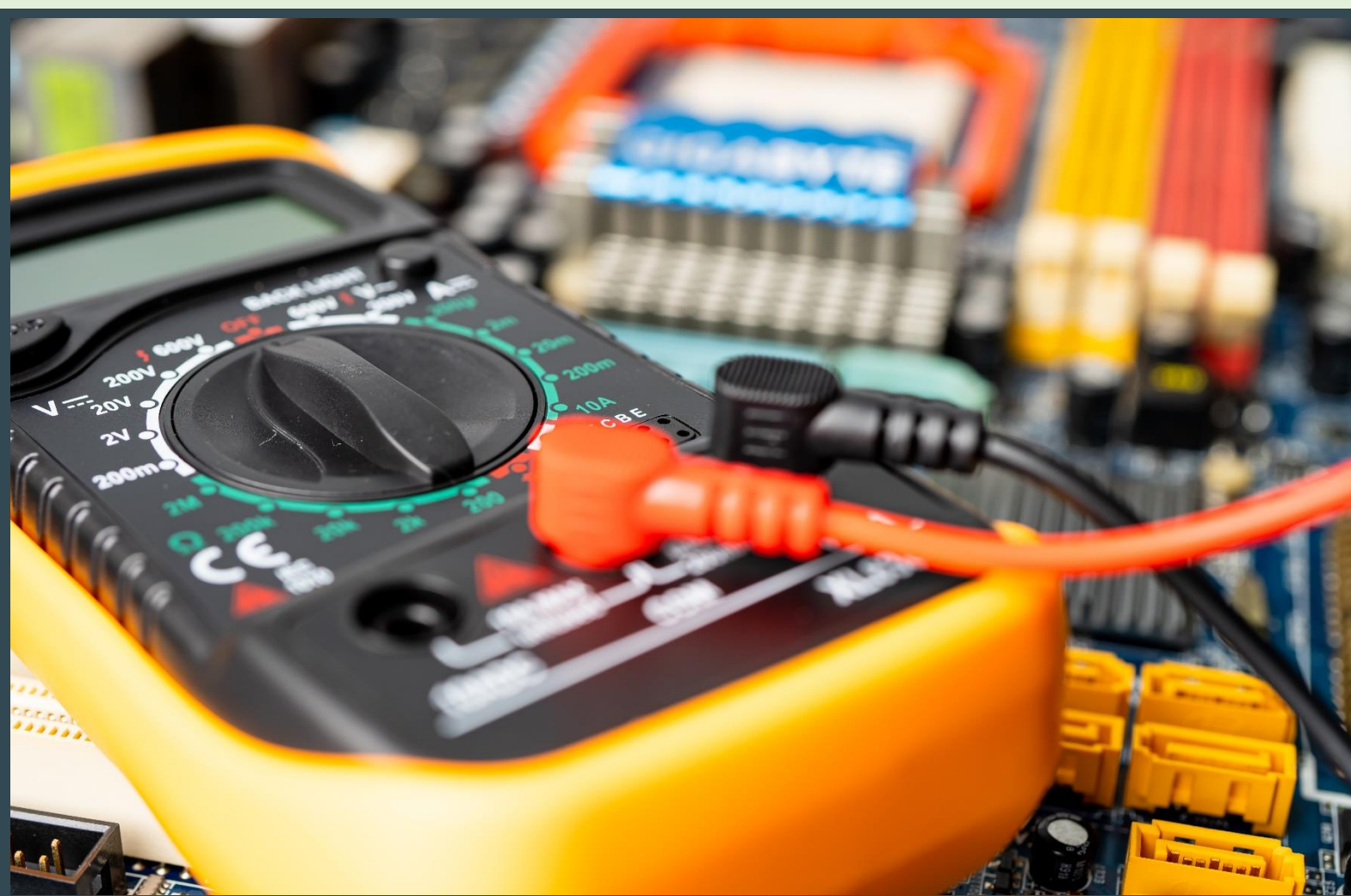


CIRCUITRY CHRONICLES

N E W S L E T T E R

ELECTRICAL & ELECTRONICS ENGINEERING



SEPTEMBER - OCTOBER 2025

CONTACT US

CHETTINAD COLLEGE OF ENGINEERING & TECHNOLOGY,
NH-67, KARUR-TRICHY HIGHWAY,
PULIYUR C.F, KARUR.
PIN CODE: 639114
URL: www.chettinadtech.ac.in

VOLUME
NO. 6
(Issue 5)

CREATIVE DESK

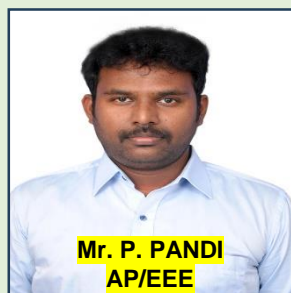
Advisory Board



Editor-in- Chief



Board of Editors



Student Editors



Table of Contents

	1	About the Department
Department Vision & Mission	2	
	3	Batch 18 - Fresher Welcome Party
Energy Audit	5	
	8	Book Free Day
Student Achievements	11	

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

About the Department

The Department of Electrical and Electronics Engineering was established in the year 2008 with the aim of combining modern teaching methods with interdisciplinary knowledge, human values and professional ethics. The department offers a unique blend of theory and practice. It provides a quality learning environment, in terms of state-of-the-art facilities, sharing and widening of knowledge through Memorandum of Understanding (MoU) with relevant industries and interacting with experts from academia and industry. The department is well equipped with state-of-the-art laboratories such as the Electrical Machines Lab, Electric Circuits Lab, Control Systems Lab, Measurement and Instrumentation Lab, Engineering Practices Lab, Power Electronics Lab, Power System Simulation Lab and Electric Drives and Control Lab. To improve practical and simulation skills, MATLAB Software with all tool boxes has been provided. Power World Simulator, MI Power, PSpice and MultiSim software programs have been provided to improve the designing ability of the students.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

VISION

- To create a thriving community where enduring student relationships flourish, fostering a culture of innovative idea development, socially responsible, and ethically driven engineers in the electrical industry.

MISSION

- To nurture students, enabling them to effectively confront professional challenges and emerge as outstanding engineers and technocrats.
- To provide a holistic and comprehensive education that ensures total quality, encompassing broad exposure and value additions.
- To engage in research within the realm of Electrical and Electronics Engineering, addressing the needs of the industry, scientific community, and society at large.

Programme Name: Batch 18 - Fresher Welcome Party

Date: 04-09-2025

Venue: C Block Seminar Hall

Organized by: Second-year EEE Students

Department: Electrical and Electronics Engineering

Description:

The Department of Electrical and Electronics Engineering hosted a lively and memorable Freshers Welcome Party for Batch 18 students on 04th September 2025 at the C Block Seminar Hall. The event was organized by the second-year EEE students, who took great effort to ensure that their juniors felt warmly welcomed into the department. The programme began with a heartfelt welcome speech, setting a cheerful tone for the occasion. The hall soon filled with energy as the stage came alive with vibrant dance performances, which received loud cheers and applause from the second-year students.

To further engage the gathering, the organizers conducted a variety of fun games that created a spirit of joy, laughter, and healthy interaction between juniors and seniors. The activities helped strengthen the bond among the students and made the event even more enjoyable.

Adding to the festive atmosphere, refreshing juice and snacks were served, giving everyone a chance to relax and interact informally. The celebration concluded with a memorable group photo session, capturing smiles, happiness, and the togetherness of the EEE family.

The Batch 18 Freshers Welcome Party was not just a programme but a celebration of new beginnings. It reflected the unity, enthusiasm, and vibrant culture of the department, leaving behind memories that will be cherished by both juniors and seniors alike.

Event Photos:





Program Name: Energy Audit

Date: 15-09-2025 to 20-09-2025

Venue: Government Hospital, Kulithalai

Student Members (III EEE): 1. P. Karthikeyan, 2. R. Rohith, 3. S. Bharathi, 4. R. Samyuktha, 5. S. Sureka
6. G. Mythili

Convener: Dr. M. Senthil kumar, Prof./EEE

Supervisor: Mr. K. Boopathi, Senior Lab Technician

Objective:

- The Energy Audit aimed to evaluate the electrical energy consumption pattern of the Government Hospital, Kulithalai, identify areas of energy wastage, and suggest practical measures to improve energy efficiency and conservation.

Description:

The Department of Electrical and Electronics Engineering organized an **Energy Audit** at the **Government Hospital, Kulithalai**, from **15th to 20th September 2025**. The primary objective was to analyze the hospital's electrical energy usage, identify inefficiencies, and propose measures for effective energy conservation. The audit team conducted a detailed load survey covering lighting, air-conditioning systems, medical and office equipment, and pumping systems. Power and energy readings were measured using portable digital instruments to assess actual consumption patterns.

The audit found that air-conditioning and lighting loads contributed the most to overall consumption. The measured average power factor was 0.85, indicating a scope for improvement through reactive power compensation. The Energy Audit team suggested several steps to improve energy efficiency at the Government Hospital, Kulithalai.

They recommended replacing all lights with LED fixtures and installing occupancy sensors in corridors and waiting areas to reduce wastage. Air conditioners should be set to 24°C and maintained regularly for better performance. To improve the power factor above 0.95, installing automatic power factor correction (APFC) panels was advised. Water pumps should be operated during off-peak hours to lower electricity costs. A 10 kW rooftop solar system was proposed to use renewable energy, and staff should be encouraged to save energy through awareness programs and display boards.

Outcome:

- The Energy Audit provided hands-on experience for students in measuring and analyzing energy parameters. The proposed recommendations are expected to reduce the hospital's monthly energy cost by 20–25% and enhance overall operational efficiency.
- This initiative showcased the department's commitment to sustainable energy practices and served as a valuable step toward real-world application of classroom knowledge.

Energy Audit Team Feedback:

The Energy Audit team expressed that the program was a highly informative and practical learning experience. It helped them understand real-time energy consumption patterns, load analysis, and methods to identify areas of energy wastage. The students gained hands-on experience in measuring electrical parameters, calculating power and energy usage, and suggesting feasible energy-saving solutions. They appreciated the opportunity to apply classroom knowledge to a real-world environment like the Government Hospital, Kulithalai. The team also conveyed that such field activities enhance technical skills, teamwork, and awareness of sustainable energy practices. Overall, the program was found to be very useful, insightful, and motivating for future energy management initiatives.

Event Photos:





Program Name: Book Free Day

Date: 17-10-2025

Class: 2nd Year, 3rd Year and Final-Year

Venue: ChettinadTech Campus

Program Objectives:

1. To enhance students' creativity, communication, and problem-solving skills through fun-filled interactive games.
2. To encourage teamwork, leadership, and collaboration among students of different year levels.
3. To develop students' quick thinking, decision-making, and innovation abilities through technical and non-technical challenges.
4. To promote a sense of peer appreciation and self-confidence by engaging students in participatory and reflective activities.

Description:

The Department of Electrical and Electronics Engineering organized a vibrant and engaging event titled "Book Free Day" on 17th October 2025, exclusively for Second-Year, Third-Year, and Final-Year students. The event aimed to provide students with a refreshing break from their regular academic routine, encouraging teamwork, creativity, and critical thinking through a series of interactive games and activities.

For the Second-Year students, the day began with exciting contests such as Rapid Fire (Electro Quiz), Identify & Connect, and Trash to Treasure, where students showcased their technical knowledge, logical reasoning, and innovative thinking by creating useful items from recyclable materials. The Third-Year students enthusiastically participated in Tech Challenge, Think Fast, and Guess from the Picture, which tested their quick decision-making, technical understanding, and visual interpretation skills. The Final-Year students took part in dynamic sessions including Role Play, Peer Appreciation Circle, Problem-Solving Challenge, and Leadership Task, designed to enhance communication, empathy, analytical ability, and leadership qualities.

The event fostered a lively and collaborative environment, allowing students to explore their creative and intellectual potential beyond textbooks. It strengthened team spirit, built confidence, and promoted a culture of joy in learning. The Book Free Day was truly a memorable and rejuvenating experience that reflected the department's commitment to holistic student development.

Program Outcomes for Students:

- Students developed improved team coordination and leadership qualities through group-based activities.
- Participants enhanced their critical thinking and analytical skills while tackling time-bound challenges.
- Students gained confidence in public speaking and interpersonal communication during role-play and peer appreciation sessions.
- The event fostered a creative and innovative mindset, motivating students to think beyond textbooks.
- Students experienced a sense of community, motivation, and positive engagement, contributing to their overall personal and professional growth.

Participants Feedback:

The “Book Free Day” received an overwhelming and positive response from students across all year levels. Participants appreciated the refreshing break from academic routines and expressed that the activities were both fun and intellectually stimulating. Many students remarked that the event fostered a strong sense of collaboration, communication, and bonding among peers. They appreciated the department’s effort in organizing such a lively and well-structured program that balanced learning with enjoyment. Overall, students described the Book Free Day as a memorable, motivating, and refreshing experience that rekindled their enthusiasm for learning beyond the classroom.

Faculty Organizer’s Feedback:

The faculty organizers expressed great satisfaction with the successful conduct of the “Book Free Day” event held on 17th October 2025. They appreciated the enthusiastic participation and energetic involvement of students from the Second, Third, and Final Years, which made the program lively and impactful. The faculty observed that the activities encouraged students to think creatively, collaborate effectively, and communicate confidently in a non-academic environment. Overall, the organizers felt that “Book Free Day” was a valuable and meaningful initiative that strengthened student–faculty relationships, nurtured creativity, and promoted experiential learning.

Event Photos:





STUDENT ACHIEVEMENTS



Elite NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)



This certificate is awarded to

VIDHIYA K

for successfully completing the course



Introduction to Semiconductor Devices

with a consolidated score of **61** %

Online Assignments	17.31/25	Proctored Exam	43.5/75
--------------------	----------	----------------	---------

Total number of candidates certified in this course: **608**

Prof. B Umashankar
Chairperson, Centre for Continued Education (CCE)
IIT Hyderabad

Jul-Oct 2025

(12 week course)

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras



Indian Institute of Technology Hyderabad



Roll No: NPTEL25EE92S364600515

To verify the certificate



No. of credits recommended: 3 or 4



NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)



This certificate is awarded to

CHOUDARY M R

for successfully completing the course



Introduction to Semiconductor Devices

with a consolidated score of **46** %

Online Assignments	15.91/25	Proctored Exam	30/75
--------------------	----------	----------------	-------

Total number of candidates certified in this course: **608**

Prof. B Umashankar
Chairperson, Centre for Continued Education (CCE)
IIT Hyderabad

Jul-Oct 2025

(12 week course)

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras



Indian Institute of Technology Hyderabad



Roll No: NPTEL25EE92S364600060

To verify the certificate



No. of credits recommended: 3 or 4



INSTITUTION INNOVATION COUNCIL

Chettinad Group

The Chettinad Group has over 90 years of experience facilitating a wide range of educational and service institutions successfully in Tamil Nadu. The group is currently responsible for 22 private and government-aided schools, Polytechnic Medical, Dentistry, Pharmaceutical, Engineering, Law and Architecture colleges.

Chettinad
College of Engineering & Technology

Chettinad
cement

Chettinad
Education & Services

Chettinad
Health City

Chettinad
Academy
of Research and Education
(Chartered by government)

Chettinad
School of
Architecture

Chettinad
Hospital & Research
Institute

Annamalai
Polytechnic College

Chettinad
School of
Law

Chettinad Sarvaaloka Education
International School

Chettinad
Hari Shree
Vidyalayam
Knowledge revolution begins

Chettinad
Vidya Mandir
Affiliated to CBSE



LIFE AT ChettinadTech



Chettinad

College of Engineering & Technology

NH - 67, Karur Trichy Highway, Puliur CF, Karur - 639 114.
Tamilnadu, India.

93450 02630 93607 02630

info@chettinadtech.ac.in / www.chettinadtech.ac.in